

#7 / 9/6/01  
Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: SHIMOYAMA et al.

PATENT APPLICATION

Serial Number: (Divisional of Appln. Serial No.  
08/970,145, filed November 13, 1997)

Prior Art Unit: 2814

Filed: Herewith

Prior Examiner: D. Wille

For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE AND METHOD  
FOR MANUFACTURING THE SAME

INFORMATION DISCLOSURE STATEMENT  
PURSUANT TO 37 CFR 1.97(b)



Director of Patents and Trademarks  
Washington, D.C. 20231

February 20, 2001

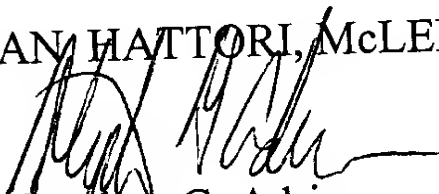
Sir:

This Information Disclosure Statement is being filed in order to comply with applicant's duty of disclosure under 37 CFR §1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 08/970,145.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, WESTERMAN, HATTORI, McLELAND & NAUGHTON, LLP

  
Stephen G. Adrian  
Reg. No. 32,878

Attorney docket No. 950971B  
1725 K Street, N.W., Ste. 1000  
Washington, D.C. 20006  
(202) 659-2930  
SGA/arf  
Enclosures: Form PTO-1449